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	Reg. No.:	
	Question Paper Code: 90527	
	B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2022.	
	Sixth/Seventh/Eighth Semester	
	Electrical and Electronics Engineering	
	EE 8691 — EMBEDDED SYSTEMS	
	(Common to Electronics and Instrumentation Engineering/Instrumentation and Control Engineering)	
	(Regulations 2017)	
7	Time: Three hours Maximum: 100 marks	
VV	I. Give two features that differ an embedded processor from a general purpose processor.	Н
2		
	processor.	
3	processor.  2. What are role of watchdog timer and real time clock?	
3	processor.  2. What are role of watchdog timer and real time clock?  3. What is voltage arbitration in Bus communication?	
3	processor.  2. What are role of watchdog timer and real time clock?  3. What is voltage arbitration in Bus communication?  4. Name the Bus suitable for broadcast and for peer to peer Communication.  5. Why does program complexity increase with reduced number of DFGs and increasing nodes?	
3 4 5	processor.  What are role of watchdog timer and real time clock?  What is voltage arbitration in Bus communication?  Name the Bus suitable for broadcast and for peer to peer Communication.  Why does program complexity increase with reduced number of DFGs and increasing nodes?  What is use of simulator in a development phase?	
3 4 5	processor.  What are role of watchdog timer and real time clock?  What is voltage arbitration in Bus communication?  Name the Bus suitable for broadcast and for peer to peer Communication.  Why does program complexity increase with reduced number of DFGs and increasing nodes?  What is use of simulator in a development phase?  Compare in-system (ISP) and In-Application Programming(IAP).	
3 4 5 6 7 8	processor.  What are role of watchdog timer and real time clock?  What is voltage arbitration in Bus communication?  Name the Bus suitable for broadcast and for peer to peer Communication.  Why does program complexity increase with reduced number of DFGs and increasing nodes?  What is use of simulator in a development phase?  Compare in-system (ISP) and In-Application Programming(IAP).	
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3 4 5 6 7 8 9	processor.  What are role of watchdog timer and real time clock?  What is voltage arbitration in Bus communication?  Name the Bus suitable for broadcast and for peer to peer Communication.  Why does program complexity increase with reduced number of DFGs and increasing nodes?  What is use of simulator in a development phase?  Compare in-system (ISP) and In-Application Programming(IAP).  What are the cause for deadlock situation in RTOS?  Why are Tasks ranked in a RTOS based system?  Why are digital keys used by smart cards stored in PROM?	

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THE A				
		PART B — $(5 \times 13 = 65 \text{ marks})$		
	11. (	Draw neatly discuss on the internal block diagram of Embedded processor and on memory devices for it.	a Typical (13)	
		Or Or		
	(	Write on any TWO to explain the added advantage achieved by of	inclusion (13)	
		(i) DMA for peripheral interface	(5)	
		(ii) Cache memory replacement policy	(4)	
		(iii) Associative mapping	(4)	
	12. (	Describe the IIC type of the serial communication BUS with control its frame format in communication.	describing (13)	
		additions of the property of the Or		
	(	Describe the CAN Bus, frame formats and communication features of CAN that make it an error free field bus.	protocol (13)	
	13. (	Discuss on	(6 + 7)	
		(i) Controlled data flow graph	(6)	
W	V	(ii) Sequential Program Model Or Explain briefly on the different phases of EDLC.		
1	4. (8			
	(	(i) Semaphores	(13)	
		(ii) Task process and threads	(4)	
		(iii) Message, Mail	(5)	
		Or	(4)	
	(t			
		(i) Round Robin Scheduling	(7)	
		(ii) Preemptive Scheduling	(6)	
1	5. (a			
		Or		
	(b	Considering one example of either a ATM machine/ Washing discuss on the embedded automation for the chosen case study.	machine (13)	
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