Reg. No. : $\square$

## Question Paper Code: 40437

B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2021.

Third Semester<br>Biomedical Engineering<br>EC 8353 - ELECTRON DEVICES AND CIRCUITS<br>(Common to Computer and Communication Engineering/<br>Electrical and Electronics Engineering/ Electronics and Instrumentation Engineering/<br>Instrumentation and Control Engineering/Robotics and Automation)

(Regulations 2017)
Time : Three hours
Maximum : 100 marks
Answer ALL questions.

## N/M/ PART A N $^{(10 \times 2=20 \text { marks }) ~}$

1. Define transition capacitance.
2. Draw the characteristics curve for LED.
3. What are the requirements for biasing circuits?
4. What is meant by negative resistance region of UJT?
5. Draw the small signal equivalent circuit of CE amplifier.
6. Why NPN transistor has a better high frequency response than the PNP transistor?
7. Mention the need for a coupling capacitor in amplifier circuits.
8. What is meant by neutralization?
9. Write the expression for input and output resistances of voltage series feedback amplifier.
10. Draw the equivalent circuit of crystal oscillator.

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\text { PART B }-(5 \times 13=65 \text { marks })
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11. (a) With neat sketches, explain the construction and L/I characteristics of Laser diode. Mention its applications.

## Or

(b) Determine the forward voltage and forward current for the PN junction diode shown in Figure 11 (b) for both ideal model (by taking $\mathrm{V}_{\mathrm{F}}=0 \mathrm{~V}$ ) and for practical model (by taking $\mathrm{V}_{\mathrm{F}}=0.7 \mathrm{~V}$ ). Also determine the voltage across the limiting resistor in each case. Assume $r_{d}^{\prime}=10 \Omega$ at the determined value of forward current.


Figure 11 (b)
12. (a) With neat sketches, explain the construction, working and characteristics of a n-channel D- MOSFET.
(b) With neat sketches, discuss about the construction, working and characteristics of IGBT.
13. (a) Draw the small signal equivalent circuit of Common Source Amplifier operated at high frequency and explain. Derive the expression for the voltage gain of the CS amplifier.

## Or

(b) Draw the small signal equivalent model for Common collector amplifier constructed using BJT and derive the expression for current gain, voltage gain, input resistance and output resistance.
14. (a) With neat circuit diagram and relevant equations, explain the working of a differential amplifier. Also derive the expression for single ended AC voltage gain of the circuit.

Or
(b) With neat diagrams, elucidate the construction, operation and frequency response characteristics of a single tuned amplifier. Also mention why potential instability occur in tuned amplifiers?

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15. (a) Draw the circuit of a Colpitt's oscillator, explain its working and also derive the condition for the frequency of oscillation.

## Or

(b) Design a phase shift oscillator using FET having $g_{m}=5000 \mu \mathrm{~S}$, $\mathrm{r}_{\mathrm{d}}=40 \mathrm{~K} \Omega$ and feedback circuit value of $\mathrm{R}=10 \mathrm{~K} \Omega$. Determine the value of $C, R_{L}$ and $R_{D}$ to have the frequency of operation as 1 KHz and $\mathrm{A}>29$.

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\text { PART C }-(1 \times 15=15 \text { marks })
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16. (a) A full wave rectifier is fed from a transformer having a center tapped secondary winding. The RMS voltage from either end of secondary to center tap is 30 V . If the diode forward resistance is $2 \Omega$ and that of the half secondary is $8 \Omega$, for a load of $1 \mathrm{~K} \Omega$, determine the power delivered to load, percentage regulation at full load, rectification efficiency and transformer utility factor (TUF) of secondary.

## Or

(b) For the common-base amplifier shown in Figure 16 (b)


Figure 16 (b)
Draw the small-signal equivalent and using those parameters determine the input impedance $\left(\mathrm{Z}_{\mathrm{i}}\right)$, Current Gain $\left(\mathrm{A}_{\mathrm{i}}\right)$, Voltage Gain $\left(\mathrm{A}_{\mathrm{v}}\right)$ as well as output impedance ( $\mathrm{Z}_{\mathrm{o}}$ ).

