

## **Basics of Electronics and Microprocessor Engineering**

### **Important 2mark questions**

1. Define PN junction diode.
2. Draw the circuit diagram for transistor as a switch.
3. Classify the amplifiers based on configurations and Q point.
4. What are the two conditions for Barhausen criterion?
5. Draw the logic symbol for two input AND gate and OR gate.
6. List out the interrupts available in 8051.
7. Draw the mode 1 input configuration signal diagram.
8. Draw the logic symbol for two input XOR gate and its truth table.
9. Define semiconductor.
10. Draw the circuit symbol for NPN and PNP transistor.
11. Define amplifier.
12. Draw the integrator circuit using Op. Amp.
13. Draw the half adder circuit.
14. Define the function of reset pin in 8051.
15. What are the various modes of 8255 IC?

### **Important 3mark questions**

1. Draw the circuit diagram for CC and CE configuration.
2. Draw the drain characteristics for an n-channel FET.
3. Define Colpitts oscillator.
4. Explain the function of ALU.
5. Define read and write control logic logic of 8255.
6. What are the types of transistor configuration? Define CB configuration.
7. Explain the operation of SR flip flop using truth table.
8. What are the various addressing modes of 8051? Define direct addressing.
9. Draw the pin diagram of 8255 IC.
10. Draw the full adder circuit.