

AP 7202 ASIC and FPGA DESIGN

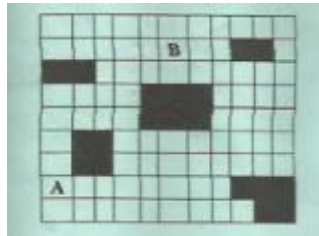
Important 13mark questions

Unit I

1. Explain the different types of ASIC.
2. With the help of a block diagram explain the architecture of PAL and PLA and implement a 4:1 MUX with the device and compare the performance.

Unit II

1. Write a pseudo code for simulated annealing method used for partitioning and explain it briefly.
2. Find routing channel between A and B in fig. Using Lee Maze algorithm and explain the algorithm.



Unit III

1. Explain in detail about the PODEM algorithm with neat sketches.
2. With a block diagram, explain the functions of BST controller.

Unit IV

1. Explain routing architecture of FPGA.
2. Discuss Kernighan Lin algorithm with a suitable example.

Unit V

1. Write short notes on Bluetooth radio/Modem.
2. Explain in detail about the system level HW-SW co-design with a neat block diagram.