

**AP 7202 ASIC and FPGA DESIGN**

**Important 2mark questions**

**Unit I**

1. Differentiate PAL and PLA.
2. What are the advantages of lambda-based design rule?
3. What are the functional blocks of FPGA?

**Unit II**

1. Give few advantages of global routing.
2. List some of the placement algorithms.
3. What are the objectives of partitioning?

**Unit III**

1. How are triggering clock pulse error identified?
2. What is fault simulation?
3. What is the standard format for interchanging information between different EDA tools?

**Unit IV**

1. Draw the basic logic cell of Actel 1 FPGA.
2. Computation processor design by FPGA is preferred over ASIC design. Why?
3. Draw ACT1 logic module.

**Unit V**

1. Enumerate the components required for embedded core testing.
2. Define SOC.
3. List the functional blocks of digital camera.